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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/941,370	08/28/2001	Hao Fang	DA01036/1363D	7060
7590 10/24/2003		EXAMINER		
Kelly K Kordzik Winstead Sechrest & Minick P.C.			BOOTH, RICHARD A	
5400 Renaissan 1201 eLM sTR			ART UNIT	PAPER NUMBER
DALLAS, TX 75270			2812	
			DATE MAILED: 10/24/200	3

Please find below and/or attached an Office communication concerning this application or proceeding.

			X //		
	Application No.	Applicant(s)	7110		
	09/941,370	FANG ET AL.			
Office Action Summary	Examiner	Art Unit	•		
·	Richard A. Booth	2812			
The MAILING DATE of this communication ap	ppears on the cover	sheet with the correspondence address	S		
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a replication of the No period for reply specified above, the maximum statutory points. - Failure to reply within the set or extended period for reply will, by statured that the same of the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	.136(a). In no event, howe ply within the statutory min d will apply and will expire S te. cause the application to	ver, may a reply be timely filed imum of thirty (30) days will be considered timely. SIX (6) MONTHS from the mailing date of this commur become ABANDONED (35 U.S.C. § 133).	nication.		
1)⊠ Responsive to communication(s) filed on <u>25</u>	July 2003 .	·	•		
·— ·	his action is non-fi	nal.			
3) Since this application is in condition for allow closed in accordance with the practice under Disposition of Claims	vance except for fo er Ex parte Quayle,	rmal matters, prosecution as to the model 1935 C.D. 11, 453 O.G. 213.	erits is		
4)⊠ Claim(s) <u>1-4 and 10</u> is/are pending in the ap	plication.				
4a) Of the above claim(s) is/are withdr		ation.			
5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1-4 and 10</u> is/are rejected.					
7) Claim(s) is/are objected to.					
8) Claim(s) are subject to restriction and	or election require	ment.			
Application Papers					
9)☐ The specification is objected to by the Examir		_			
10)☐ The drawing(s) filed on is/are: a)☐ acc	cepted or b) object	red to by the Examiner.			
Applicant may not request that any objection to	the drawing(s) be he	Id in abeyance. See 37 CFR 1.85(a).			
11) The proposed drawing correction filed on					
If approved, corrected drawings are required in		tion.			
12) The oath or declaration is objected to by the E	=xaminer.				
Priority under 35 U.S.C. §§ 119 and 120					
13) Acknowledgment is made of a claim for forei	ign priority under 3	5 U.S.C. § 119(a)-(d) or (f).			
a) ☐ All b) ☐ Some * c) ☐ None of:					
1. Certified copies of the priority docume			· · ·		
2. Certified copies of the priority docume					
Copies of the certified copies of the prince application from the International I * See the attached detailed Office action for a limit is a second content.	Bureau (PCT Rule	17.2(a)).	ge		
14) Acknowledgment is made of a claim for dome	stic priority under	35 U.S.C. § 119(e) (to a provisional ap	plication).		
a) The translation of the foreign language (provisional applicatestic priority under	ion has been received. 35 U.S.C. §§ 120 and/or 121.			
Attachment(s)					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	4) 5) 6)	Notice of Informal Patent Application (PTO-15			

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DETAILED ACTION

The finality of the office action mailed 1/23/03 has been withdrawn.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Cappelletti et al., U.S. Patent 5,637,520 in view of Takebuchi, U.S. Patent 6,417,051 or Holler et al., U.S. Patent 4,780,424 or Wristers et al., U.S. Patent 5,674,788.

Cappelletti et al. shows the invention substantially as claimed including a method comprising the steps of: providing a portion 35' of a dual gate oxide in a periphery area of the memory cell; simultaneously providing a dual gate oxide 61 in a core area of the memory device and completing the dual gate oxide 35" in the periphery area (see Figures 10-12 and column 4-lines 20-65).

Cappelletti et al. fails to expressly disclose providing a nitridation process in both the core area and periphery area of the memory device.

Takebuchi discloses providing a nitridation process in both the core area and periphery area of the memory device (see fig. 4D-4F and col. 12-lines 4-40).

Additionally, Holler et al. discloses that nitridation processes enhance the quality of gate oxides (see col. 5-lines 12-15), and Wristers et al. discloses nitridation improves the properties of oxides (see col. 3-lines 40-65). In view of this disclosure, it would have

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been obvious to one of ordinary skill in the art at the time the invention was made to modify the process of Cappelletti et al. so as to include an additional nitridation process after forming the oxides as suggested by Takebuchi, Holler, or Wristers et al. because the nitridation allows for a gate film of longer endurance.

Claims 2 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Cappelletti et al., U.S. Patent 5,637,520 in view of Takebuchi, U.S. Patent 6,417,051 or Holler et al., U.S. Patent 4,780,424 or Wristers et al., U.S. Patent 5,674,788 as applied to claim 1 above, and further in view of Lee, U.S. Patent 5,175,120.

Cappelletti et al. and Nakata are applied as above but fail to expressly disclose depositing a layer of polysilicon in a core and peripheral area; depositing a layer of ONO layer over the polysilicon; and removing the layer of ONO and poly from the peripheral area.

Lee discloses a conventional process for simultaneous memory and peripheral formation including forming a polysilicon layer 24 and a ONO layer 26 and removing both of these layers from the peripheral region 10 (see Figures 2A-2B and column 3-lines 4-55) and a subsequent process to form a second polysilicon layer in the memory and peripheral areas. In view of this disclosure, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the process so as to include memory and peripheral gate formation process of Lee in the reference of Cappelletti et al. modified by Nakata because this is shown to be conventional————fabrication for memory and peripheral circuits.

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Response to Arguments

Applicant's arguments with respect to claims 1-2 and 10 have been considered but are most in view of the new ground(s) of rejection.

Allowable Subject Matter

Claims 3-4 are allowed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard A. Booth whose telephone number is 308-3446. The examiner can normally be reached on Monday-Thursday from 7:30-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Niebling can be reached on 308-3325. The fax phone numbers for the organization where this application or proceeding is assigned are 308-7724 for regular communications and 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 308-1782.

Richard A. Booth Primary Examiner Art Unit 2812